



**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



**COURSE DESCRIPTION**

**1. Program identification information**

1.1 Higher education institution	National University of Science and Technology Politehnica Bucharest
1.2 Faculty	Electronics, Telecommunications and Information Technology
1.3 Department	Electronic Devices, Circuits and Architectures
1.4 Domain of studies	Electronic Engineering, Telecommunications and Information Technology
1.5 Cycle of studies	Bachelor/Undergraduate
1.6 Programme of studies	Microelectronics, Optoelectronics and Nanotechnologies

**2. Date despre disciplină**

2.1 Course name (ro) (en)	Circuite electronice fundamentale 2 - Proiect Fundamental Electronic Circuits 2 - Project						
2.2 Course Lecturer							
2.3 Instructor for practical activities	Prof. dr. ing. Florin Drăghici, Conf. dr. Mihaela Pantazică						
2.4 Year of studies	3	2.5 Semester	1	2.6. Evaluation type	V	2.7 Course regime	Ob
2.8 Course type	D	2.9 Course code	04.D.05.O.006	2.10 Tipul de notare	Nota		

**3. Total estimated time (hours per semester for academic activities)**

3.1 Number of hours per week	1	Out of which: 3.2 course	0.00	3.3 seminary/laboratory	1
3.4 Total hours in the curricula	14	Out of which: 3.5 course	0	3.6 seminary/laboratory	14
Distribution of time:					hours
Study according to the manual, course support, bibliography and hand notes Supplemental documentation (library, electronic access resources, in the field, etc) Preparation for practical activities, homework, essays, portfolios, etc.					34
Tutoring					0
Examinations					2
Other activities (if any):					0
3.7 Total hours of individual study	36.00				
3.8 Total hours per semester	50				
3.9 Number of ECTS credit points	2				

**4. Prerequisites (if applicable) (where applicable)**



**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



4.1 Curriculum	<p>Completion and/or promotion of the following subjects:</p> <ul style="list-style-type: none"><li>• Basics of electrical engineering</li><li>• Electronic devices</li><li>• Fundamental electronic circuits</li><li>• Passive components and circuits</li><li>• Computer Aided Graphics - CAD Techniques for Electronics</li><li>• Spice models</li><li>• Measurements in electronics and telecommunications</li><li>• CAD Techniques for designing electronic modules</li></ul>
4.2 Results of learning	<ul style="list-style-type: none"><li>• Gaining knowledge in the following domains:<ul style="list-style-type: none"><li>◦ Fundamentals of electrical engineering</li><li>◦ Electronic devices</li><li>◦ Electronic circuits</li><li>◦ Analysis of electrical circuits</li><li>◦ Passive components and circuits</li><li>◦ Circuit simulation</li><li>◦ Measurements in electronics and telecommunications</li><li>◦ Computer-aided design</li></ul></li></ul>

**5. Necessary conditions for the optimal development of teaching activities (where applicable)**

5.1 Course	<ul style="list-style-type: none"><li>• Project type</li></ul>
5.2 Seminary/ Laboratory/Project	<ul style="list-style-type: none"><li>• The project classes (analysis and design) will take place in a room with special equipment that must include: Video projector, blackboard, internet connection.</li></ul>

**6. General objective** *(Referring to the teachers' intentions for students and to what the students will be thought during the course. It offers an idea on the position of course in the scientific domain, as well as the role it has for the study programme. The course topics, the justification of including the course in the curricula of the study programme, etc. will be described in a general manner)*

Familiarizing students with design techniques specific to analog circuits. Use of knowledge acquired in the disciplines of Electronic Devices, Fundamental Electronic Circuits, Passive Components and Circuits, Spice Models, and CAD Techniques related to: diodes and transistors (models and parameters), amplification stages, negative feedback, multistage amplifiers, linear voltage regulators, oscillators, computer-aided design of analog electronic modules of medium complexity. The proposed project themes are focused on circuit topologies of medium complexity used in engineering practice: amplifiers, voltage regulators, oscillators, etc.

Design of the electrical and interconnection structure in a given technology. Development of documentation for manufacturing.

Elaboration of the technical documentation for the developed project.



**7. Competences** *(Proven capacity to use knowledge, aptitudes and personal, social and/or methodological abilities in work or study situations and for personal and professional growth. They reflect the employers requirements.)*

<b>Specific Competences</b>	<ul style="list-style-type: none"><li>• The ability to design electronic circuits of low/medium complexity and implement them to a given specification in a given technology using the CAD techniques.</li><li>• The ability to evaluate circuit performance through analytical computations and simulations.</li><li>• The ability to select and use electronic components (active and passive) according to manufacturer's documentation (data sheets) and operating conditions of the designed circuit.</li><li>• The ability to implement an interconnection structure in a specific technology.</li><li>• The ability to prepare documentation for the manufacturing of a circuit designed in a specific technology.</li><li>• The ability to present the obtained results synthetically and in a domain-specific vocabulary.</li></ul>
<b>Transversal (General) Competences</b>	<ul style="list-style-type: none"><li>• Coordinates efforts with others to solve specific situations with varying degrees of difficulty.</li><li>• Independence and critical thinking: ability to think in technical terms, independently research and analyze data, and derive and present new solutions.</li><li>• Ability to analyze and synthesize: present acquired knowledge in synthetic form, as the result of a systematic analysis process.</li><li>• Adherence to the principles of academic ethics.</li><li>• Correctly cites bibliographic sources used as references in own communicated and published work.</li><li>• Applies elements of emotional intelligence in appropriate social-emotional interactions with real/academic/professional situations and demonstrates self-control and objectivity in decision-making or stressful situations.</li><li>• Respects deadlines in order to coordinate with the entire team.</li></ul>

**8. Learning outcomes** *(Synthetic descriptions for what a student will be capable of doing or showing at the completion of a course. The learning outcomes reflect the student's accomplishments and to a lesser extent the teachers' intentions. The learning outcomes inform the students of what is expected from them with respect to performance and to obtain the desired grades and ECTS points. They are defined in concise terms, using verbs similar to the examples below and indicate what will be required for evaluation. The learning outcomes will be formulated so that the correlation with the competences defined in section 7 is highlighted.)*



**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



<b>Knowledge</b>	<p><i>The result of knowledge acquisition through learning. The knowledge represents the totality of facts, principles, theories and practices for a given work or study field. They can be theoretical and/or factual.</i></p> <ul style="list-style-type: none"><li>• Designs block diagrams based on the requirements stated in a design specification/topic of low/medium complexity.</li><li>• Selects a circuit-level implementation variant based on electrical and technological design specifications.</li><li>• Demonstrates circuit operation and compliance with design requirements through analytical computations.</li><li>• Implements the design in a CAD environment at the schematic and interconnection structure level.</li><li>• Simulates the operation of the designed circuit based on the models of the selected electronic components.</li><li>• Verifies in the CAD environment the correspondence between the simulated schematic diagram and the interconnection structure.</li><li>• Generates files for manufacturing the interconnection structure on a PCB in an industrial facility.</li><li>• Documents the design, simulation, assembly and testing activities.</li></ul>
<b>Skills</b>	<p><i>The capacity to apply the knowledge and use the know-how for completing tasks and solving problems. The skills are described as being cognitive (requiring the use of logical, intuitive and creative thinking) or practical (implying manual dexterity and the use of methods, materials, tools and instrumentation).</i></p> <ul style="list-style-type: none"><li>• Selects and groups relevant information in a given context.</li><li>• Appropriately applies specific principles to design electronic modules of low/medium complexity.</li><li>• Suggests practical applications for the electronic components and circuits studied.</li><li>• Recognizes the importance of model parameters for the electrical operation of electronic devices and circuits.</li><li>• Solves practical problems using theoretical knowledge.</li><li>• Experimentally verifies (through simulation) the solutions determined. Interprets causal relationships adequately.</li><li>• Analyzes and compares electronic component specifications for use in a circuit.</li><li>• Identifies solutions and develops project plans.</li><li>• Formulates conclusions about experiments conducted.</li><li>• Justifies the solutions identified.</li><li>• Works well in a team environment.</li></ul>



<b>Responsability and autonomy</b>	<i>The student's capacity to autonomously and responsibly apply their knowledge and skills.</i>
	<ul style="list-style-type: none"><li>• Selects and evaluates appropriate bibliographic sources.</li><li>• Respects principles of academic ethics and correctly cites bibliographic sources used.</li><li>• Demonstrates responsiveness to new learning contexts.</li><li>• Demonstrates collaboration with other colleagues and faculty staff in carrying out educational activities.</li><li>• Demonstrates independence in organizing the learning context or in choosing a solution for a problem-based situation.</li><li>• Contributes to improving the quality of social life through new solutions related to his field of activity.</li><li>• Is aware of the value of his/her contribution in the field of engineering to the identification of viable/sustainable solutions to solve problems in social and economic life (social responsibility).</li><li>• Applies the principles of professional ethics/deontology in analyzing the technological impact of proposed solutions in his field of activity on the environment.</li><li>• Analyzes and exploits opportunities for entrepreneurial development in the field.</li><li>• Demonstrates coping skills in real-life situations.</li></ul>

**9. Teaching techniques** *(Student centric techniques will be considered. The means for students to participate in defining their own study path, the identification of eventual fallbacks and the remedial measures that will be adopted in those cases will be described.)*

Based on the analysis of the students' learning characteristics and their specific needs, the teaching process uses both expository (lecture, exposition) and conversational-interactive teaching methods based on models of discovery learning facilitated by direct and indirect exploration of reality (experimentation, demonstration, modeling), but also by action-oriented methods such as exercises, practical activities and problem solving.

Instruction is based on both classical methods and multimedia resources and techniques. The complex issues are demonstrated on the blackboard. The materials used are: presentations, assignment collections and course notes, data sheets; most materials are available in electronic form through the project website and Moodle. Pictures and diagrams are used in the presentations so that the information presented is easy to understand and internalize.

The "Fundamental Electronic Circuits 2 - Project" subject area includes information and hands-on activities designed to support students in their learning efforts and to develop optimal collaborative and communicative relationships in a climate conducive to discovery learning. It is also a bridge between theoretical concepts acquired in the various disciplines and engineering practice.

The practice of active listening and assertive communication skills, as well as mechanisms for building feedback, are encouraged to regulate behavior in different situations and to adapt the pedagogical approach to the learning needs of students.

## 10. Contents

<b>PROJECT</b>		
<b>Crt. no.</b>	<b>Content</b>	<b>No. hours</b>



**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



1	<ul style="list-style-type: none"><li>• Presentation of the project.</li><li>• Presentation of the project's topics.</li><li>• Presentation of the design data/specifications.</li><li>• Systematization of knowledge about electronic devices and circuits.</li></ul>	1
2	<ul style="list-style-type: none"><li>• Selection criteria for the general schematic of the designed circuit, based on the circuit topologies used in technical practice.</li><li>• Block diagram of the circuit.</li></ul>	1
3	<ul style="list-style-type: none"><li>• Typical schematics and design algorithms for: Bias circuits (voltage references, current sources, etc.), input stages, output stages, etc.</li><li>• Selection of components. Use of data sheets.</li><li>• DC and AC Analysis: Determination of static and dynamic parameters of the circuit.</li></ul>	3
4	<ul style="list-style-type: none"><li>• Editing the electrical circuit diagram.</li><li>• Simulation of the operation of the circuit. Selection of models for circuit components.</li><li>• The (final) selection of components and packages for the components (see data sheets).</li><li>• Bill of materials (BOM).</li></ul>	2
5	<ul style="list-style-type: none"><li>• Design of the interconnection structure for the developed circuit. Dimensioning of the interconnect copper traces. Minimizing the length of the traces and the area occupied by the components on the PCB.</li><li>• Finding solutions for heat dissipation problems.</li><li>• Cross-reference schematic diagram-layout.</li><li>• Generation of files for manufacturing (GERBER and Excellon).</li></ul>	6
6	<ul style="list-style-type: none"><li>• Presentation of the electrical design activity.</li></ul>	0.5
7	<ul style="list-style-type: none"><li>• Evaluation of the electrical design activity.</li></ul>	0.5
	<b>Total:</b>	14



## Bibliography:

1. [Course: 04-ETTI-L-A3-S1: Circuite electronice fundamentale 2 - Proiect \(Seria E - 2024\). | POLITEHNICA București Elearning](#)
2. F. Drăghici, "Amplificatoare. Noțiuni de proiectare", Editura Matrix Rom, București, 2021, ISBN 978-606-25-0665-0, 111 pagini.
3. P. R. Gray, P. J. Hurst, S. H. Lewis, R. G. Meyer, Analysis and Design of Analog Integrated Circuits, J. Wiley & Sons, 2001;
4. G. Brezeanu, F. Drăghici, Circuite electronice fundamentale, Ed. Niculescu, București, 2013;
5. G. Brezeanu, F. Draghici, F. Mitu, G. Dilimot, Circuite electronice fundamentale - probleme, Editura Rosetti Educational, Bucuresti, editia II-2008;
6. G. Brezeanu, F. Draghici, F. Mitu, G. Dilimot, Dispozitive electronice - probleme, Editura Rosetti Educational, Bucuresti, 2009;
7. P. Svasta, V. Golumbeanu, C. Ionescu, Al. Vasile, Componente electronice pasive –Rezistoare, Proprietăți, Construcție, Tehnologie, Aplicatii., Ed. Cavallioti, Bucuresti 2011;
8. P. Svasta, Al. Vasile, Ciprian Ionescu, V. Golumbeanu, "Componente și circuite pasive – Condensatoare", Proprietăți, Construcție, Tehnologie, Aplicatii., Ed. Cavallioti, București 2010;
9. Norocel Codreanu, "Metode avansate de investigație a structurilor "PCB""", Modelare și simulare, integritatea semnalelor, Ed. Cavallioti, București 2009; G. Băjeu, Gh. Stancu, Generatoare de semnale sinusoidale, Ed. Tehnică, București, 1979;
10. Dascălu, A. Rusu, M. Profirescu, I. Costea, Dispozitive și circuite electronice, Ed. Didactică și Pedagogică,
11. A. M. Manolescu, A. Manolescu, Analog Integrated Circuits, Ed. Electronica 2000, București, 2011;
12. D. Self, Audio Power Amplifier Design Handbook, Fourth edition, Newnes, 2006;
13. G. A. Rincon-Mora, Voltage References – from Diodes to Precision High-Order Bandgap Circuits, John Wiley, 2001;
14. I. Ristea, C. A. Popescu, Stabilizatoare de tensiune, Ed. Tehnică, 1983;
15. M. Ciugudean, Proiectarea unor circuite electronice, Ed. Facla, 1983;
16. A. Lăzăroiu, Ș. Naicu, Generatoare de semnal analogice și digitale - scheme practice, Matrixrom, 2000;
17. <http://www.dce.pub.ro;>
18. Norocel Codreanu, Ciprian Ionescu, Mihaela Pantazică, Alina Marcu, "Tehnici CAD de realizare a modulelor electronice - suport de curs și laborator", Editura Cavallioti, PIM, Iași, Decembrie 2017;
19. [http://www.cetti.ro/v2/tehniciCAD.php;](http://www.cetti.ro/v2/tehniciCAD.php)
20. [http://www.cetti.ro/v2/labtie.php;](http://www.cetti.ro/v2/labtie.php)
21. [http://www.elect2eat.eu;](http://www.elect2eat.eu)
22. [www.ipc.org](http://www.ipc.org).

## 11. Evaluation

Activity type	11.1 Evaluation criteria	11.2 Evaluation methods	11.3 Percentage of final grade
11.4 Course			





11.5 Seminary/laboratory/project	<ul style="list-style-type: none"><li>• The ability to select a solution that meets the requirements of an electrical design topic.</li><li>• Design skills, evaluation through analytical computations and simulations of a circuit.</li></ul>	<ul style="list-style-type: none"><li>• Grading students in each phase of the project based on how well they meet the requirements of each stage.</li></ul>	50%
	<ul style="list-style-type: none"><li>• Design and layout skills in a CAD environment.</li><li>• Skills in the preparation of manufacturing files.</li><li>• Skills in the preparation of technical documentation.</li></ul>	<ul style="list-style-type: none"><li>• Grading students in each phase of the project based on how well they meet the requirements of each stage.</li></ul>	40%
	<ul style="list-style-type: none"><li>• Presentation of all projects' activities and results obtained.</li></ul>	<ul style="list-style-type: none"><li>• Final evaluation of the projects' activities and results obtained.</li></ul>	10%
11.6 Passing conditions			
<ul style="list-style-type: none"><li>• Obtaining 50% of the total grade.</li><li>• Obtaining 50% of the grade related to the activity during the semester.</li></ul>			

**12. Corroborate the content of the course with the expectations of representatives of employers and representative professional associations in the field of the program, as well as with the current state of knowledge in the scientific field approached and practices in higher education institutions in the European Higher Education Area (EHEA)**

The development of analog, digital and mixed circuits is a test of maturity for an engineer. The current achievements of companies in the field of electronics are based on design activities that, together with the new technologies, allow them to offer on the market devices and electronic systems with the smallest dimensions, with the greatest autonomy and number of functions at the lowest prices.

The project "Basic Electronic Circuits 2" is an introduction of the future engineer to a specific activity performed in a development company. The activity carried out in the project is exclusively for the application and processing of the knowledge acquired in the specialized courses of the first two years. Knowledge of the fundamentals of electrical engineering, electronic devices and circuits, passive components and computer-aided design (CAD) are used.

Market requirements are met so that the future engineer has an idea of the design and manufacturing process of an electronic circuit/module, which, in this case, uses only discrete components. The student attending "Basic Electronic Circuits 2 - Project" is introduced to industrial design, where he/she must find an optimal solution within technological and time limits constraints.

In this way, the student acquires the skills required by the current needs of the market, which will enable him/her to be quickly employed in an electronics company after graduation.





**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



The subject is thus part of the policy of National University of Science and Technology Politehnica Bucharest, both in terms of content and structure, and in terms of international openness offered to students.

Date

Course lecturer

Instructor(s) for practical activities

25.09.2025

Conf. Dr. Mihaela Pantazică

Prof. dr. ing. Florin Drăghici

Date of department approval

Head of department

22.10.2025

Prof. Dr. Claudius Dan

Date of approval in the Faculty Council

Dean